BENHA UNIVERSITY FACULTY OF ENGINEERING (SHOUBRA) ELECTRONICS AND COMMUNICATIONS ENGINEERING



## ECE 444 Industrial Electronics (2022 - 2023) 1st term

Lecture 4: Analog Signal Conditioning.

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## **Outlines:**

### Analog Signal Conditioning.

### Analog Signal Conditioning Categories.

Voltage Divider Circuit.

Wheatstone Bridge Circuit.

Design Guideline.

## Analog Signal Conditioning (S/C):

Signal conditioning refers to operation performed on signals to convert them to a form that is suitable to interface with other elements in the process-control loop.

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- A sensor measures a variable by converting information about that variable into a dependent signal.
- We often describe the effect of the signal conditioning by the term transfer function.
- Signal conditioning Categories:
  - 1) Signal Level and bias changes.
  - 2) Linearization.
  - 3) Conversion.
  - 4) Filtering.
  - 5) Impedance Matching.

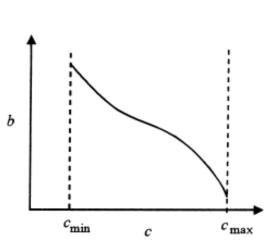
## 1-Signal level and bias changes:

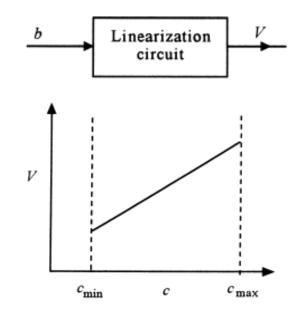
One of the most common types of signal conditioning involves adjusting the level (magnitude) and bias (zero value) of some voltage representing a process variable.

For example, some sensor output voltage may vary from 0.2 to 0.6 V equipment to which this sensor output must be connected perhaps requires a voltage that varies from 0 to 5 V for the same variation of the process:

subtracting 0.2 → a zero shift, or a bias adjustment (0 to 0.4)
 multiply the voltage by 12.5 → amplification (0 to 5)

- The designer has a little choice of the characteristics of a sensor (Often has a nonlinear T.F.).
- A linearization circuit is difficult to design and has a narrow range.
- Now software based linearization techniques are used.





## 3. Conversion:

- > To convert one type of electrical variation into another (e.g.  $\Delta R$  to  $\Delta V$  or  $\Delta I$ )
- For signal transmission (4-20 mA current loop) (V to I and I to V)
- For digital interface, The use of computers in process control requires conversion of analog data into a digital format by integrated circuit devices called analog-to-digital converters (ADCs). An analog signal conversion is usually required to adjust the analog signal to match the input requirement of the ADC

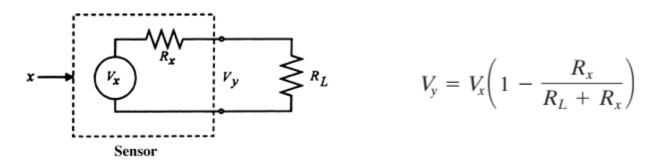
e.g. Sensor output 20 – 90 mV ADC range 0 - 5 V

## 4. Filtering:

- Often, spurious signals of considerable strength are present in the industrial environment, such as the 60-Hz line frequency signals.
- Motor start transients may also cause pulses and other unwanted signals in the process-control loop.
- In many cases, it is necessary to use high-pass, low-pass, Band-pass, band-reject or notch filters to eliminate unwanted signals from the loop.

## 5. Impedance Matching:

- Impedance matching is an important element of signal conditioning when transducer internal impedance or line impedance can cause errors in measurement of a dynamic variable.
- Loading introduces uncertainty in the amplitude of a voltage as it is passed through the measurement process. (Loading Effect).



If loading is ignored, serious errors can occur in expected outputs of circuits and gains of amplifiers. Passive circuit:

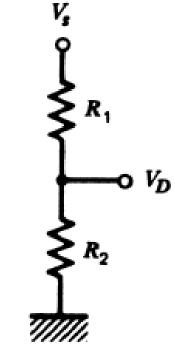
### Voltage Divider Circuit:

The simple voltage divider can often be used to convert resistance variation into voltage variation.

 $> V_D = V_s R_2 / (R_1 + R_2)$ > R<sub>1</sub> or R<sub>2</sub> may be a sensor

> Notes:

- Nonlinearity of the equation
- Loading effect
- R1 & R2 power rating



## Voltage Divider Circuit:

EXAMPLE 2

**E** The divider of Figure 4 has  $R_1 = 10.0 \text{ k}\Omega$  and  $V_s = 5.00 \text{ V}$ . Suppose  $R_2$  is a sensor whose resistance varies from 4.00 to 12.0 k $\Omega$  as some dynamic variable varies over a range. Then find (a) the minimum and maximum of  $V_D$ , (b) the range of output impedance, and (c) the range of power dissipated by  $R_2$ .

Solution

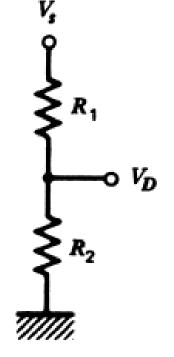
**a.** The solution is given by Equation (2). For  $R_2 = 4 \text{ k}\Omega$ , we have

$$V_D = \frac{(5 \text{ V})(4 \text{ k}\Omega)}{10 \text{ k}\Omega + 4 \text{ k}\Omega} = 1.43 \text{ V}$$

For  $R_2 = 12 \text{ k}\Omega$ , the voltage is

$$V_D = \frac{(5 \text{ V})(12 \text{ k}\Omega)}{10 \text{ k}\Omega + 12 \text{ k}\Omega} = 2.73 \text{ V}$$

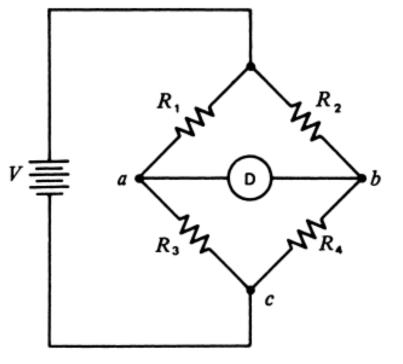
- **b.** Thus, the voltage varies from 1.43 to 2.73 V.
- c. The range of output impedance is found from the parallel combination of  $R_1$  and  $R_2$  for the minimum and maximum of  $R_2$ . Simple parallel resistance computation shows that this will be from 2.86 to 5.45 k $\Omega$ .
- **d.** The power dissipated by the sensor can be determined most easily from  $V^2/R_2$ , as the voltage across  $R_2$  has been calculated. The power dissipated varies from 0.51 to 0.62 mW.



## Wheatstone Bridge Circuit:

To convert impedance variations into voltage variations.
 One of the advantages of the bridge for this task is that it can be designed so the voltage produced varies around zero.
 To measure impedance precisely.

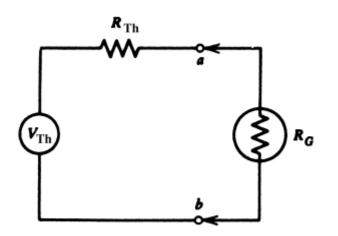
> 
$$\Delta V = V_a - V_b$$
 (voltage offset)  
 $= V_s \frac{(R_3 R_2 - R_1 R_4)}{(R_1 + R_3)(R_2 + R_4)}$   
> Null condition:  
 $R_3 R_2 = R_1 R_4$ 

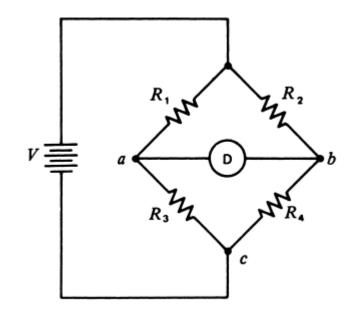


## Using Galvanometer to detect the offset:

 In some cases, a highly sensitive galvanometer with a relatively low impedance may be used
 When a galvanometer is used for a null detector, it is convenient to use the Thévenin equivalent circuit of the bridge

$$V_{\rm Th} = V \frac{R_3 R_2 - R_1 R_4}{(R_1 + R_3)(R_2 + R_4)}$$
$$R_{\rm Th} = \frac{R_1 R_3}{R_1 + R_3} + \frac{R_2 R_4}{R_2 + R_4}$$
$$I_G = \frac{V_{\rm Th}}{R_{\rm Th} + R_G}$$





## Using Galvanometer to detect the offset:

**EXAMPLE** A bridge circuit has resistance of  $R_1 = R_2 = R_3 = 2.00 \text{ k}\Omega$  and  $R_4 = 2.05 \text{ k}\Omega$  and a 5.00-V supply. If a galvanometer with a 50.0- $\Omega$  internal resistance is used for a detector, find the offset current.

Solution

From Equation (9), the offset voltage is  $V_{\rm Th}$ .

$$V_{\rm Th} = 5 \,\mathrm{V} \,\frac{(2 \,\mathrm{k}\Omega)(2 \,\mathrm{k}\Omega) - (2 \,\mathrm{k}\Omega)(2.05 \,\mathrm{k}\Omega)}{(2 \,\mathrm{k}\Omega + 2 \,\mathrm{k}\Omega)(2 \,\mathrm{k}\Omega + 2.05 \,\mathrm{k}\Omega)}$$
$$V_{\rm Th} = -30.9 \,\mathrm{mV}$$

We next find the bridge Thévenin resistance from Equation (10):

$$R_{\rm Th} = \frac{(2 \,\mathrm{k}\Omega)(2 \,\mathrm{k}\Omega)}{(2 \,\mathrm{k}\Omega + 2 \,\mathrm{k}\Omega)} + \frac{(2 \,\mathrm{k}\Omega)(2.05 \,\mathrm{k}\Omega)}{(2 \,\mathrm{k}\Omega + 2.05 \,\mathrm{k}\Omega)}$$
$$R_{\rm Th} = 2.01 \,\mathrm{k}\Omega$$

Finally, the current is given by Equation (11):

$$I_G = \frac{-30.9 \text{ mV}}{2.01 \text{ k}\Omega + 0.05 \text{ k}\Omega}$$
$$I_G = -15.0\mu\text{A}$$

## Bridge resolution

It is the resistance change in one arm of the bridge that causes an offset voltage (offset current) that is equal the resolution of the detector.

> Gives a limit to min. measurable resistance change.

> Seen as overall accuracy of the instrument

## Bridge resolution

**EXAMPLE** A bridge circuit has  $R_1 = R_2 = R_3 = R_4 = 120.0-\Omega$  resistances and a 10.0-V supply. **6** Clearly, the bridge is nulled, as Equation (8) shows. Suppose a  $3\frac{1}{2}$  digit DVM on a 200-mV scale will be used for the null detector. Find the resistance resolution for measurements of  $R_4$ .

### Solution

On a 200-mV scale, the DVM measures from 000.0 to 199.9 mV, so the smallest measurable change is 0.1 mV, or 100  $\mu$ V. So, we need to find out how much  $R_4$  has changed from 120  $\Omega$  to create this much off null voltage.

We simply use Equation (6), with  $R_4$  changed to some unknown value so that 100  $\mu$ V results:

$$100 \,\mu\text{V} = \frac{(120 \,\Omega)(10 \,\text{V})}{120 \,\Omega + 120 \,\Omega} - \frac{R_4(10 \,\text{V})}{120 \,\Omega + R_4}$$

This equation is a bit of an algebraic challenge to solve, but eventually we find

$$R_4=119.9952\,\Omega$$

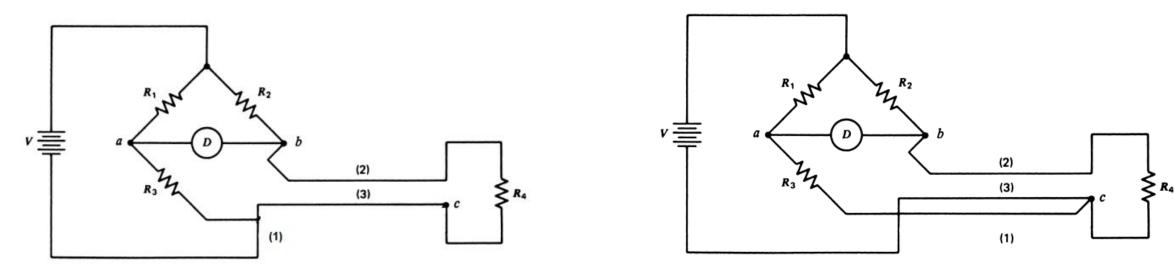
So the smallest change in resistance that can be measured is

$$\Delta R_4 = 120 \ \Omega - 119.9952 \ \Omega = 0.0048 \ \Omega$$

A bridge offset of  $+100 \,\mu\text{V}$  is caused by a reduction of  $R_4$ . It follows that a bridge offset of  $-100 \,\mu\text{V}$  would be caused by an increase of  $R_4$ .

## Bridge Compensation:

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- A Problem: In many process-control applications, a bridge circuit may be located at considerable distance from the sensor whose resistance changes are to be measured. The resistance of the wires (2) & (3) must be considered to minimize the error in measurement
- For remote sensor applications, this compensation system is used to avoid errors from lead resistance.

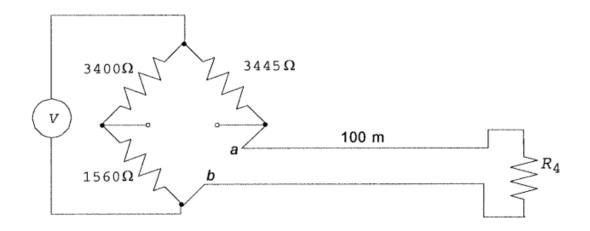


## Bridge Compensation:

9 A bridge circuit is used with a sensor located 100 m away. The bridge is not lead compensated, and the cable to the sensor has a resistance of 0.45  $\Omega/\text{ft}$ . The bridge nulls with  $R_1 = 3400 \Omega$ ,  $R_2 = 3445 \Omega$ , and  $R_3 = 1560 \Omega$ . What is the sensor resistance?

#### Solution

A diagram will help you understand this problem. The circuit is,



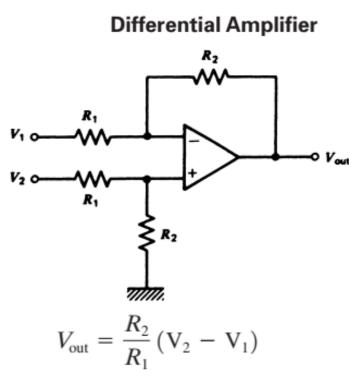
If you use the null equation to find  $R_4$ , it will give the resistance from *a* to *b* in the schematic, which includes the two 100 m lead resistances. Thus these must be subtracted to find the actual sensor resistance.

 $R_{ab} = (3445 \ \Omega)(1560 \ \Omega)/(3400 \ \Omega) = 1580.6 \ \Omega$ 

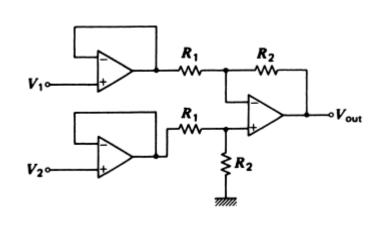
but the lead resistance is,

 $R_{\text{lead}} = 2(100 \text{ m})(0.3048 \text{ m/ft})(0.45 \Omega/\text{ft}) = 295.3 \Omega$ So the actual sensor resistance is,

 $R_4 = 1580.6 \ \Omega - 295.3 \ \Omega = 1285.3 \ \Omega$ 

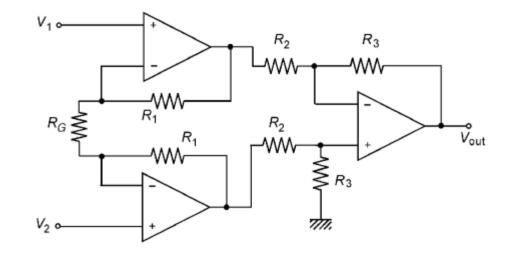


#### Instrumentation Amplifier

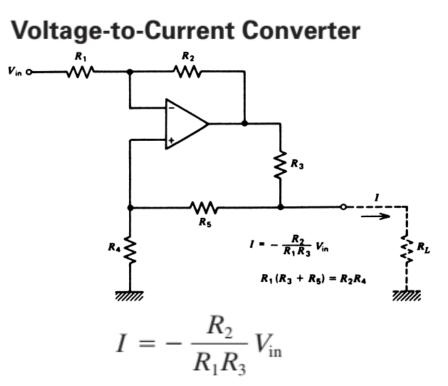


 $V_{\text{out}} = \frac{R_2}{R_1} \left( \mathbf{V}_2 - \mathbf{V}_1 \right)$ 

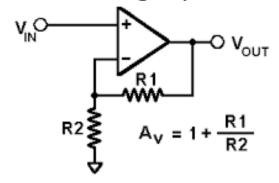
#### Instrumentation Amplifier



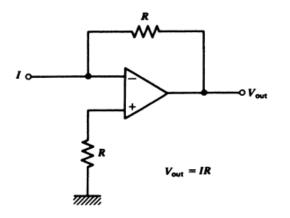
 $V_{\text{out}} = \left(1 + \frac{2R_1}{R_G}\right) \left(\frac{R_3}{R_2}\right) (V_2 - V_1)$ 



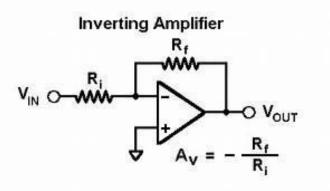




### **Current-to-Voltage Converter**



 $V_{\rm out} = -IR$ 



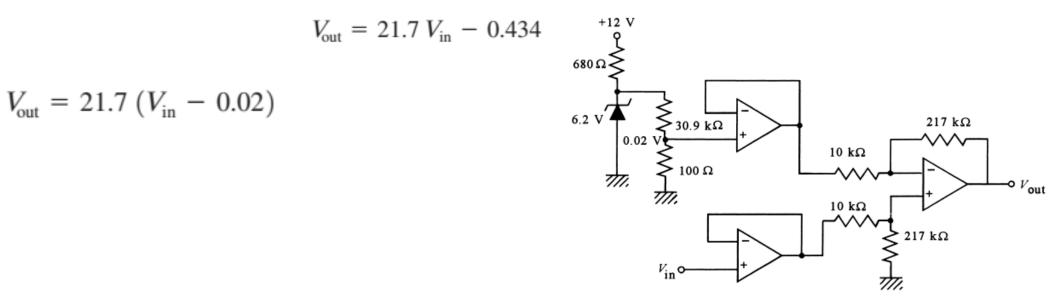
EXAMPLE A sensor outputs a range of 20.0 to 250 mV as a variable varies over its range. Develop signal conditioning so that this becomes 0 to 5 V. The circuit must have very high input impedance.

Solution

 $V_{\rm out} = mV_{\rm in} + V_0$ 

 $0 = m(0.02) + V_0$ 5 = m(0.25) + V\_0

We get m = 21.7 and  $V_0 = -0.434$  V using standard algebra. The equation is



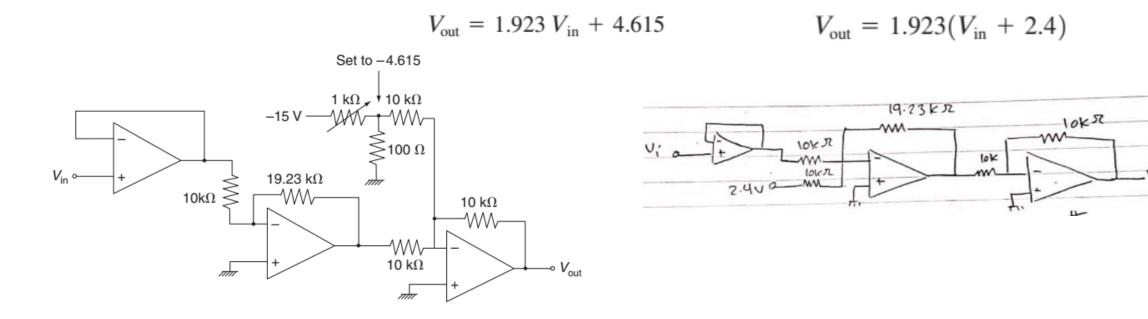
**EXAMPLE** A sensor outputs a voltage ranging from -2.4 to -1.1 V. For interface to an analog-todigital converter, this needs to be 0 to 2.5 V. Develop the required signal conditioning.

Solution

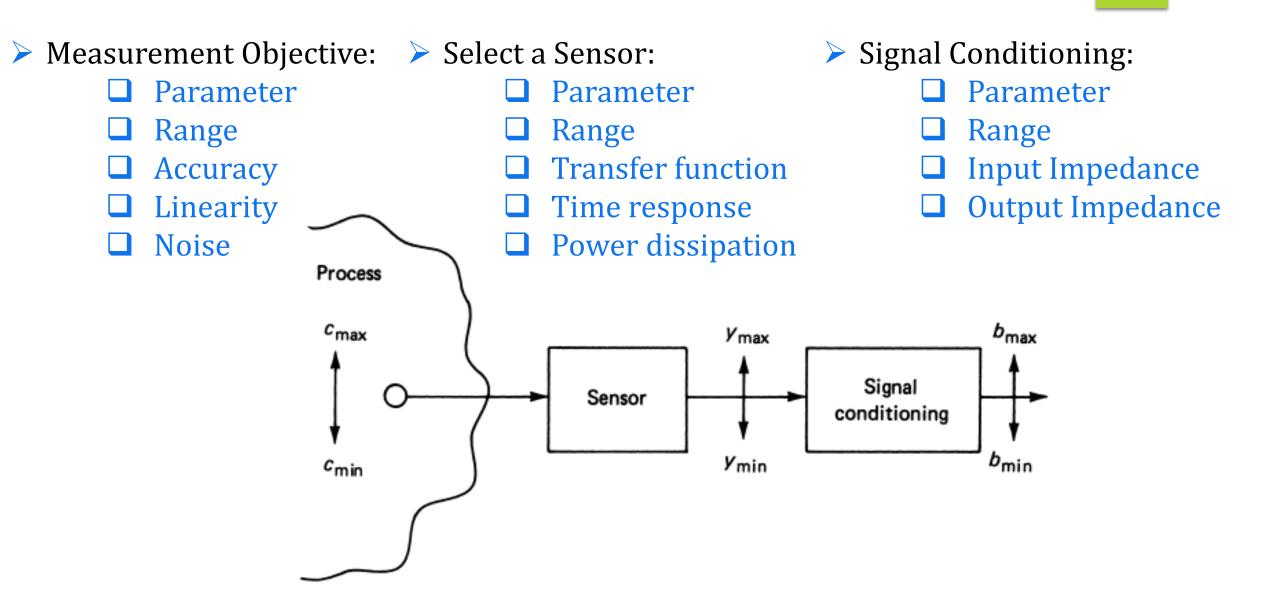
 $V_{\rm out} = mV_{\rm in} + V_0$ 

 $0 = -2.4m + V_0$ 2.5 = -1.1m + V\_0

The transfer function equation is thus



## Design Guidelines:



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## Design Guidelines:

**EXAMPLE** Temperature is to be measured in the range of 250°C to 450°C with an accuracy of  $\pm 2$ °C. **25** The sensor is a resistance that varies linearly from 280  $\Omega$  to 1060  $\Omega$  for this temperature range. Power dissipated in the sensor must be kept below 5 mW. Develop analog signal conditioning that provides a voltage varying linearly from -5 to +5 V for this temperature range. The load is a high-impedance recorder.

#### Solution

Following the guidelines, let us first identify all the elements of the problem.

#### Measured Variable Parameter: Temperature

Range: 250° to 450°C Accuracy: ±2°C Noise: unspecified

### Sensor Signal

Parameter: resistance Transfer function: linear Time response: unspecified *Range:* 280  $\Omega$  to 1060  $\Omega$ , linear *Power:* maximum 5 mW dissipated in sensor

### Signal Conditioning

Parameter: voltage, linear

Range: -5 to +5 V

*Input impedance:* keep power in sensor below 5 mW *Output impedance:* no problem, high-impedance recorder

## Design Guidelines:

5×103 - I (280) => I = 4.2 mA P=I2R Solu! : [2 (660) => I = 2.17 mA 1) The design must always keep The Sensor Current below 2mA let we make it I max - I mit DR: 280-2 660 2 -2-4 → linear relation NV: -5 -> 5 V Vont = m Rg + U -5 = m(28)+U (I) <=> m= 0.0128 , U= -8.58 (1) 5 = m(1060)+U Vont = 0.0128 Rs - 8.58)

### first use Rs Sensor with of and and Ra Take and of Current Imax - Int ->ImA 162 4--0.001 RS a MM ImA Then Guillete The Gt:. The remain gain: 0.0128 = 12.8 8 58 SIDILA lok52 1KR . NM 781R #

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# END OF LECTURE

# **BEST WISHES**